

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Othmar Leitner, et al.	Art Unit :	2822
Patent No. :	7,566,624	Examiner :	Kevin Picardat
Issue Date :	July 28, 2009	Conf. No. :	3015
Serial No. :	10/579,487		
Filed :	May 16, 2006		

Title : METHOD FOR THE PRODUCTION OF TRANSISTOR STRUCTURES WITH LDD

Attn.: Certificate of Corrections Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

TRANSMITTAL OF REQUEST FOR CERTIFICATE OF CORRECTION

Applicant hereby requests that a certificate of correction be issued for the above patent in accordance with the attached request.

All errors sought to be corrected were made in printing by the Patent and Trademark Office, and no fee is believed to be due.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

December 22, 2009  
Date: \_\_\_\_\_

/Paul Pysher/

\_\_\_\_\_  
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Page 1 of 1

PATENT NO. : 7,566,624  
APPLICATION NO : 10/579,487  
DATED : JULY 28, 2009  
INVENTOR(S) : OTHMAR LEITNER

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Cover page, Column 2, Abstract, Line 4:  
Delete [sidewalls] and Insert - -sidewalls- -

Column 6, Claim 20, Line 30:  
Delete [at] and Insert - -dopant at- -

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